

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/885,761	KUWAZAWA, KAZUNOBU	
Examiner Stanetta D. Isaac		Art Unit 2812	Page 1 of 1	

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-5,770,508	06-1998	Yeh et al.	438/305
B	US-4,855,247	08-1989	Ma et al.	438/305
C	US-5,153,145	10-1992	Lee et al.	438/305
D	US-6,087,234	07-2000	Wu, Shye-Lin	438/299
E	US-6,335,252	01-2002	Oishi et al.	438/303
F	US-6,506,650	01-2003	Yu, Bin	438/301
G	US-6,541,328	04-2003	Whang et al.	438/231
H	US-5,679,589	10-1997	Lee et al.	438/305
I	US-6,432,805	08-2002	Paton et al.	438/592
J	US-2002/0115258	08-2002	Whang et al.	438/303
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)	
U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, Lattice Press, Vol. I, pages 522-535	
V		
W		
X		

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.